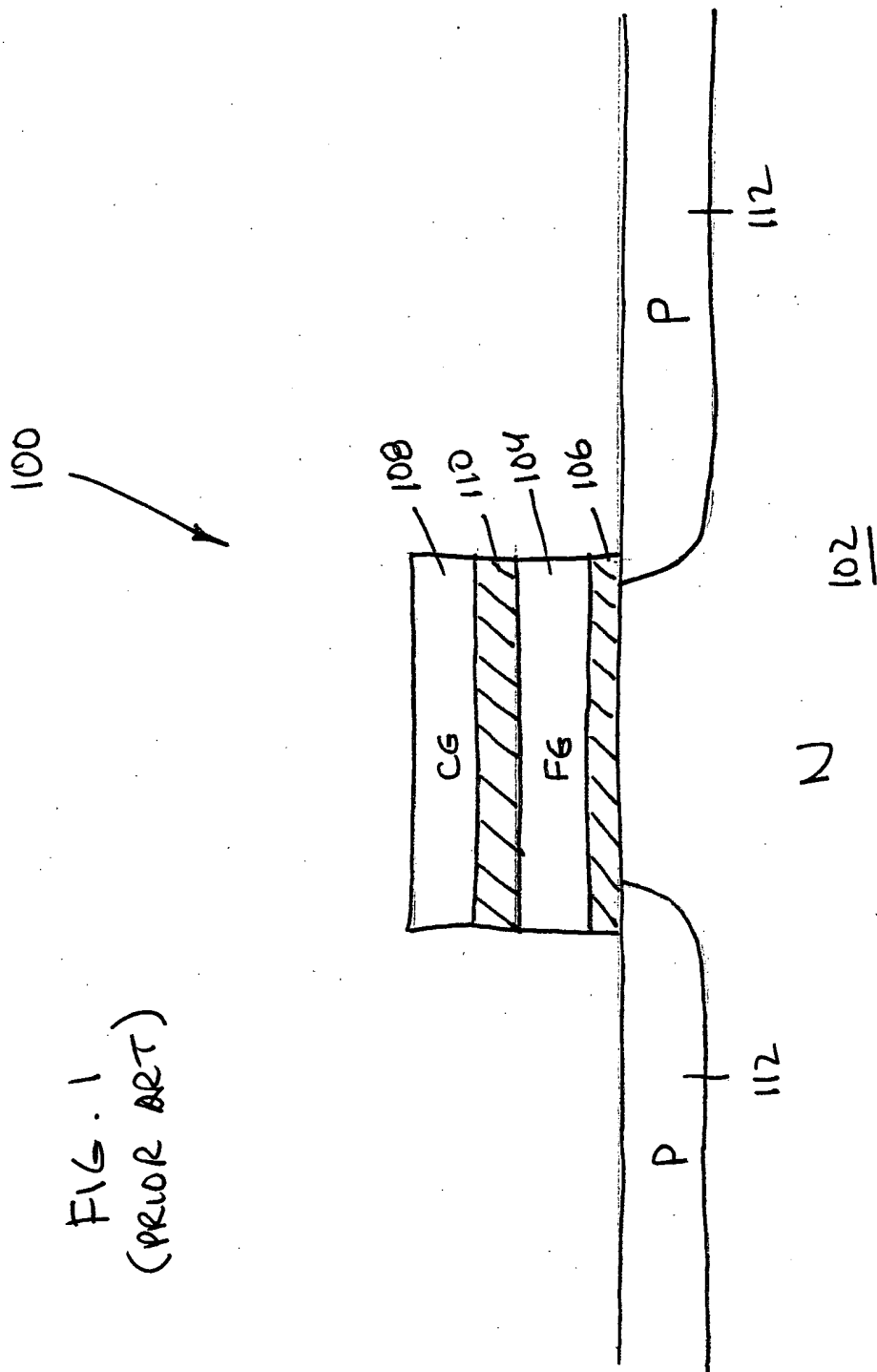


FIG. 1
(PRIOR ART)



PMOS Gate Current vs. Gate Voltage

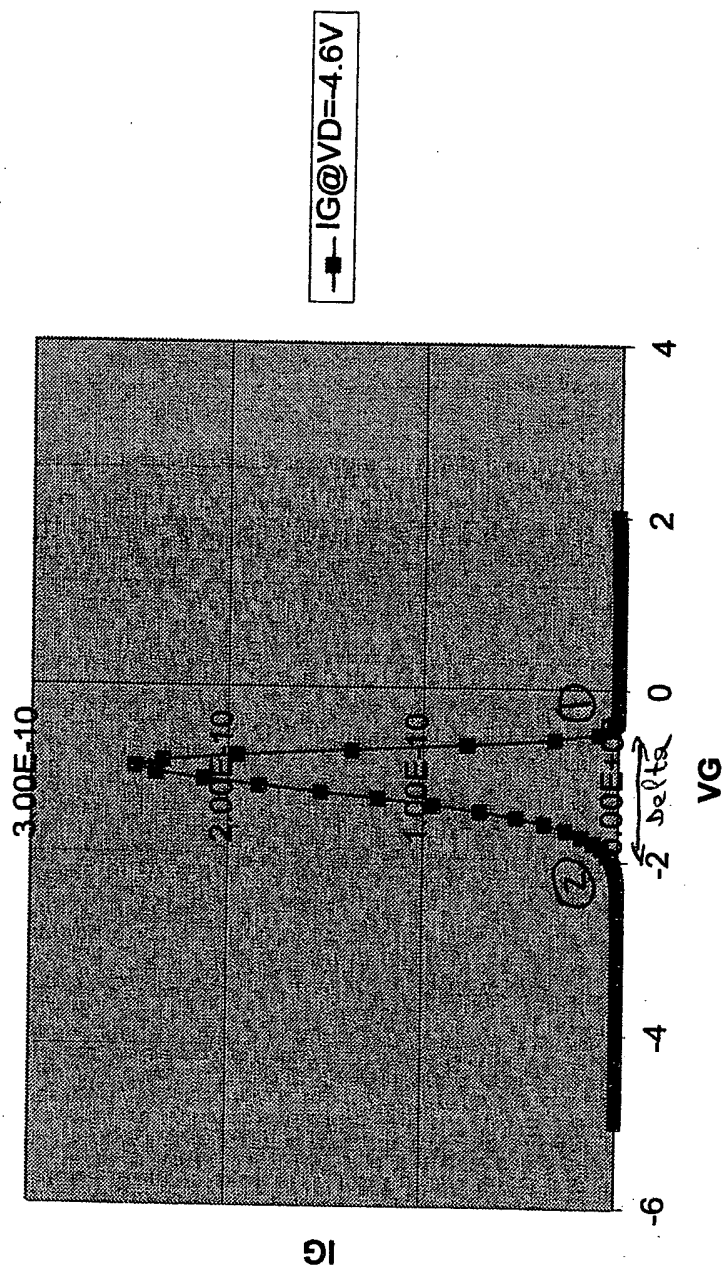
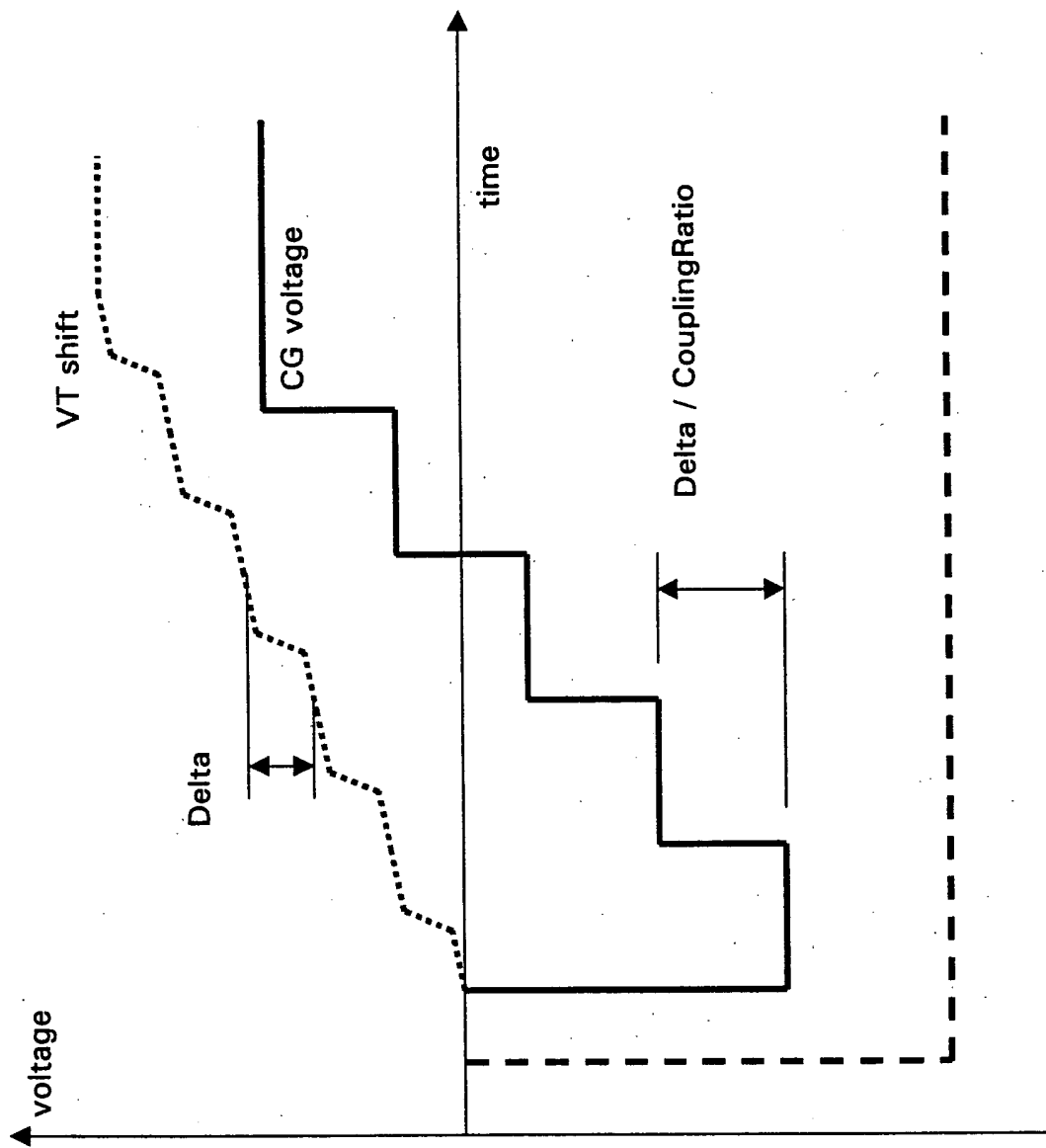


FIG. 2



CG voltage, Drain voltage, VT shift VS. Time

FIG. 3